

**F O R . I M M E D I A T E . R E L E A S E**

**Spectrum Waveform Design Studio Selected by the European Commission Joint Research Centre for Interoperable Security Solutions within the WINTSEC Project**

Software defined radio technology expected to address wireless public safety communications challenges for European Union

**Burnaby, B.C., Canada – March 25, 2008** - Spectrum Signal Processing by Vecima ("Spectrum"), a provider of software defined radio (SDR) technology, today announced that the European Commission Joint Research Centre (JRC) Sensors Radar Technologies and Cyber security Unit (SERAC) will use Spectrum's *flexComm*<sup>™</sup> Waveform Design Studio<sup>®</sup> for the research and development of software defined radio capability in the Wireless Interoperability for Security (WINTSEC) project. WINTSEC explores a mixture of complementary solutions to overcome the barriers for wireless interoperability across different security agencies, enabling first responders with incompatible legacy radios to communicate in a crisis situation.

The JRC will be using the Waveform Design Studio to achieve goals in the following areas:

- To support a possible improvement of European Union (EU) communications interoperability in the area of security and first responders
- To enhance understanding of emerging technologies, mesh networks and wideband communications
- To better assess future threats to critical network infrastructures

"The JRC is focused on solving some critical public safety communications challenges in Europe," said Mark Briggs, VP Marketing at Spectrum. "The Waveform Design Studio provides a cost-effective route for the JRC to access SDR technology. The Waveform Design Studio provides an integrated "IF to Ethernet" solution with complete run-time software, including a fully licensed Software Communications Architecture (SCA) software stack, a comprehensive suite of development tools, system examples and sample applications."

The Waveform Design Studio incorporates a single Xilinx<sup>®</sup> Virtex-4<sup>™</sup> field programmable gate array and multiple Texas Instruments TMS320C6416<sup>™</sup> digital signal processing devices, all interconnected through high performance communications fabrics integrated into an Intel<sup>®</sup>-based workstation. The Waveform Design Studio supports two receive and two transmit channels at industry standard 10.7, 21.4 and 70 MHz IF with bandwidths in excess of 30 MHz. It also supports "RF to Ethernet" for HF and VHF frequency bands.

For more information on the Waveform Design Studio, please visit [www.spectrumsignal.com/products](http://www.spectrumsignal.com/products).

**ABOUT THE JOINT RESEARCH CENTRE**

The mission of the JRC is to provide customer-driven scientific and technical support for the conception, development, implementation and monitoring of EU policies. As a service of the European Commission, the JRC functions as a reference centre of science and technology for the Union. Close to the policy-making process, it serves the common interest of the Member States, while being independent of special interests, whether private or national.

The SERAC Unit is one of the units of the Institute for the Protection and Security of the Citizen (IPSC). It contributes to EU policies related to the security of Citizens by applying secure information and embedded, smart sensing technologies. The areas of research deal with information and sensing technologies for the detection, mitigation and management of man made threats and natural hazards to critical infrastructures.

For more information on the JRC, please visit <http://ec.europa.eu/dgs/jrc/>, IPSC at <http://ipsc.jrc.ec.europa.eu/>, or SERAC Unit at <http://serac.jrc.it/>.

**ABOUT SPECTRUM SIGNAL PROCESSING BY VECIMA**

Spectrum Signal Processing is a leading supplier of software defined platforms for defense electronics applications. Spectrum's products and services are optimized for military communications, signals intelligence, surveillance, electronic warfare and satellite communications applications. Spectrum's integrated, application-ready products provide its customers with compelling time-to-market and performance advantages while mitigating program risk and cost parameters. Spectrum also provides applications engineering services and modified commercial-off-the-shelf platforms to the US Government, its allies and its prime contractors. For

more information on Spectrum and its *flexComm* product line, please visit [www.spectrumsignal.com](http://www.spectrumsignal.com). Spectrum is part of Vecima Networks Inc.

Vecima Networks Inc. ("Vecima") (TSX:VCM) designs, manufactures and sells products that enable broadband access to cable, wireless and telephony networks. Vecima's hardware products incorporate original embedded software to meet the complex requirements of next-generation, high-speed digital networks. Service providers use Vecima's solutions to deliver services to a converging worldwide broadband market, including what are commonly known as "triple play" (voice, video and data) and "quadruple play" (voice, video, data and wireless) services. Vecima's solutions allow service providers to rapidly and cost-effectively bridge the final network segment that connects the system directly to end users, commonly referred to as "the last mile", by overcoming the bottleneck resulting from insufficient carrying capacity in legacy, last mile infrastructures. Vecima's products are directed at three principal markets: Data over Cable, Broadband Wireless and Digital Video. The Company has also developed and continues to focus on developing products to address emerging markets such as Voice over Internet Protocol, fiber to the home and IP video. For more information on Vecima, please visit [www.vecimanetworks.com](http://www.vecimanetworks.com).

#### **FORWARD-LOOKING SAFE HARBOUR STATEMENT**

Certain statements in this news release may constitute forward-looking statements which involve known and unknown risks, uncertainties and other factors which may cause the actual results, performance or achievements of the Company, or industry results, to be materially different from any future results, performance or achievements expressed or implied by such forward-looking statements. When used in this news release, such statements are generally identified by the use of such words as "may", "will", "expect", "believe", "plan", "intend" and other similar terminology. These statements reflect Vecima's current expectations regarding future events and operating performance and speak only as of the date of this news release. Forward-looking statements involve significant risks and uncertainties, should not be read as guarantees of future performance or results, and will not necessarily be accurate indications of whether or not such results will be achieved. A number of factors including, but not limited to, the factors discussed under "Risk Factors" in the Company's Annual Information Form dated September 25, 2007 available on SEDAR ([www.sedar.com](http://www.sedar.com)), could cause actual results to differ materially from the results discussed in the forward-looking statements. Although the forward-looking statements contained in this news release are based upon what management of the Company believes are reasonable assumptions, the Company cannot assure investors that actual results will be consistent with these forward-looking statements. These forward-looking statements are made as of the date of this news release, and the Company assumes no obligation to update or revise them to reflect new events or circumstances.

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*flexComm* and *quicComm* are trademarks and Waveform Design Studio is a registered trademark of Vecima Networks Inc. Xilinx is a registered trademark and Virtex-4 is a trademark of Xilinx Inc. Intel is a registered trademark of Intel Corporation. TMS320C6416 is a registered trademark of Texas Instruments. Other product and company names mentioned may be trademarks and/or registered trademarks of their respective holders.

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